



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Warren M. Farnworth

Serial No.: 10/043,362

Filed: January 9, 2002

For: STEREOLITHOGRAPHIC
METHODS FOR FABRICATING
HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR
DEVICES INCLUDING
STEREOLITHOGRAPHICALLY
FABRICATED HERMETIC PACKAGES

Confirmation No.: 7080

Examiner: D. Zarneke

Group Art Unit: 2827

Attorney Docket No.: 2269-4212.1US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

February 6, 2003
Date

Signature

Deidra Pfeil

Name (Type/Print)

LETTER TO THE CHIEF DRAFTSMAN

Box Issue Fee
Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant submits herewith revised FIG. 9 which correct errors in the drawings. Specifically, FIG. 9 has been revised to delete reference numerals "30" and "120" with appropriate lead lines. FIG. 9 has also been revised to add the reference numeral --120-- with appropriate lead line; Attached is a copy of the drawing with the proposed changes marked in red.

No new matter has been added. Approval of the proposed revisions is respectfully requested.

Respectfully submitted,



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Date: February 6, 2003
BGP/djp

Enclosures: Drawing with changes marked in red
Document in ProLaw

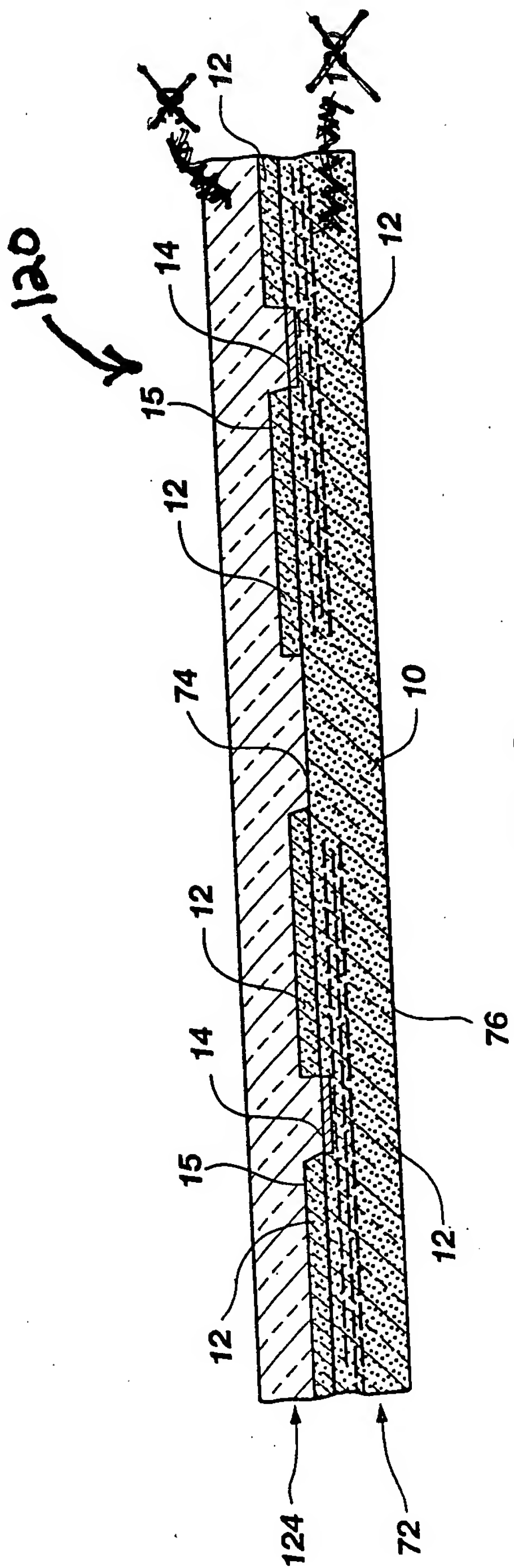


Fig. 9

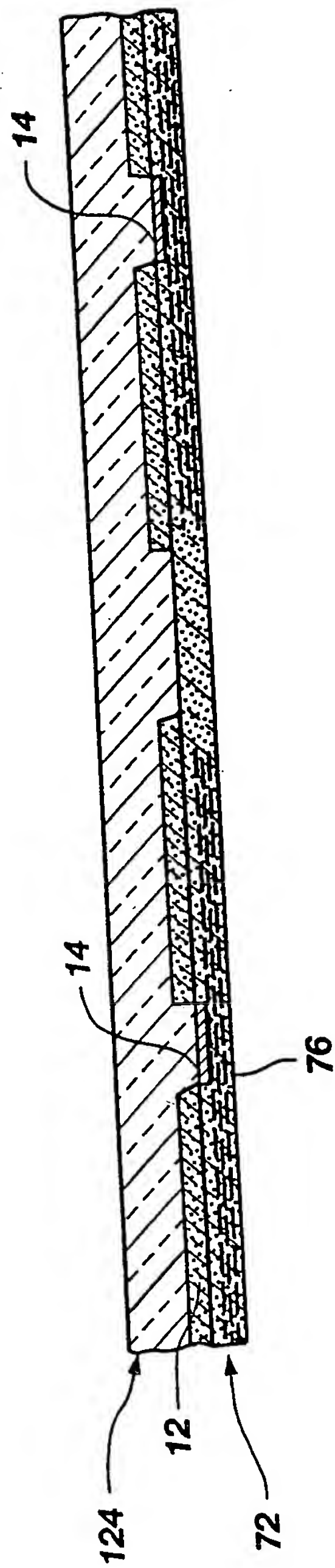


Fig. 10



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TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached please find the replacement formal drawing FIG. 9 for this application.

Respectfully submitted,

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